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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/918,967	07/31/2001	Scott-Thanh D. Ngo	TI-32407	2559
23494	7590	11/01/2005	EXAMINER	
TEXAS INSTRUMENTS INCORPORATED			HARPER, KEVIN C	
P O BOX 655474, M/S 3999			ART UNIT	
DALLAS, TX 75265			PAPER NUMBER	
			2666	

DATE MAILED: 11/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/918,967

Applicant(s)

NGO, SCOTT-THANH D.

Examiner

Kevin C. Harper

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 31 July 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,2,10,11,14,16,17,19-23,26 and 27 is/are rejected.
- 7) ☒ Claim(s) 3-9,12,13,15,18,24 and 25 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 31 July 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 7/01.
- ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: \_\_\_\_\_.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 14, 16-17 and 19-22 are rejected under 35 U.S.C. 102(b) as being anticipated by Krivosheev et al. (US 3,995,105).

1. Regarding claim 14, Krivosheev discloses a semiconductor device (col. 7, lines 5-15) comprising a pulse sequence counter (fig. 1, item 10; fig. 5, item 64; col. 18, lines 4-7) coupled to an input/output node (fig. 1, item 9; col. 17, lines 41-55) to count the number of pulses received at the node, a first storage location to store a first count result (col. 18, lines 5-6; note: the count is stored therein - col. 18, lines 7-9), and a pulse generator (fig. 10, item 62) for generating a specified length sequence of pulses which is one less than the number of pulses received at the input/output node (col. 18, lines 15-25; note: when the number of pulses received in the second group is one, the number of transmitted pulses is the number of received pulses in the first group minus one.)
2. Regarding claim 16, a second sequence of pulses is received at a second input/output node (fig. 1, item 7 or 8).
3. Regarding claim 17, a second storage location stores a second count result (fig. 5, item 65).
4. Regarding claim 19, the device includes a controller (fig. 5, item 63) coupled to and controlling the first storage location, counter and the pulse generator (col. 18, lines 15-25).
5. Regarding claims 20-22, the semiconductor device is a microcontroller and a microprocessor (col. 18, lines 12-20; fig. 1, item 17) and a finite state machine (fig. 7, col. 21, lines 23-30).

Claims 23 and 26-27 are rejected under 35 U.S.C. 102(b) as being anticipated by Lehwalder et al. (US 2003/0204657).

6. Regarding claim 23, Lehwalder discloses a system comprising a processor (fig. 4, item 4) coupled to a sequence of codecs (items 20 and 21) to process digital data (para. 27, lines 12-16), a controller (figs. 2 or 4, item 42) for controlling communications between the processor and the sequence of codecs (note: primary and secondary codecs; para. 21), where each codec comprises a port coupled to the processor and the controller (fig. 4, ports having Sdata) and a semiconductor device (paras. 3-4 and 34; note: the semiconductor device is capable of distributed device identifier number assignment and device counting through programming or design - MPEP 2114).

7. Regarding claims 26-27, the semiconductor device operates each time the system is powered-up or reset (para. 21, lines 1-4 and 11-13).

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-2 and 10-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kizu (US 2004/0179511) in view of Downey, Jr. et al. (US 2002/0126574).

8. Regarding claim 1, Kizu discloses a method for device counting (note: “distributed device identifier number assignment” is only found in the preamble and has not been given patentable weight \*1; Kizu, fig. 1, registration step) comprising transmitting from a slave and receiving at a

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master a first sequence of received pulses (fig. 1, slave M and slave S1), determining a unique device identifier based on the sequence of received pulses (para. 56, lines 6-9; para. 58, last four lines; para 61, lines 1-4; para. 56, lines 3-6; note: the communication includes inherent physical pulses or burst to achieve communication - para. 81), transmitting and receiving a second sequence of pulses (fig. 1, slaves S2-S4) and determining a device count based on the first and second sequences of received pulses (para. 76; note: the received sequence of pulses is any group of data received at the master).

9. However, Kizu does not disclose serially connected chain of devices. Downey, Jr. discloses serially connected slave devices (fig. 6). Therefore, it would have been obvious to one skilled in the art at the time the invention was made to have serially connected slave devices in the invention of Kizu in order to provide a simple device structure (Downey, para. 25, last three lines). (\*1 - A preamble is generally not accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).

10. Regarding claim 2, first and second memory locations are initialized before receiving the first sequence of pulses (fig. 2; para. 68).

11. Regarding claims 10-11, the first received sequence and the first transmitted sequence are received and transmitted over different input/output connections (fig. 3, items 11-12; para. 81; note: the transmission from a slave is received by a master).

***Allowable Subject Matter***

12. Claims 3-9, 12-13, 15, 18 and 24-25 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Harper whose telephone number is 571-272-3166. The examiner can normally be reached weekdays from 11:00 AM to 7:00 PM ET.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Seema S. Rao, can be reached at 571-272-3174. The centralized fax number for the Patent Office is 571-273-8300. For non-official communications, the examiner's personal fax number is 571-273-3166 and the examiner's e-mail address is kevin.harper@uspto.gov.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications associated with a customer number is available through Private PAIR only. For more information about the PAIR system, see portal.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kevin C. Harper

October 28, 2005